

## **PSS 4000 Light Curtain with PSENopt Ladder Diagram**

**PILZ**  
THE SPIRIT OF SAFETY

### **Product**

Type: FS\_LightCurtain, FS\_OutputFBL  
Name: PSS 4000, Blocks, PAS4000, PLC, LD  
Manufacturer: Pilz GmbH & Co. KG, Safe Automation

### **Document**

Release Number: 02  
Release Date: 09 February 2017

## Document Revision History

Release	Date	Changes	Chapter
01	2016-11-30	Creation	all
02	2017-02-09	Correction	all

## Validity of Application Note

This present Application Note is valid until a new version of the document is published. This and other Application Notes can be downloaded in the latest version and for free from [www.pilz.com](http://www.pilz.com). For a simple search, use our [content document \(1002400\)](#) or the [direct search function](#) in the download area.

## Exclusion of liability

We have taken great care in compiling our application note. It contains information about our company and our products. All statements are made in accordance with the current status of technology and to the best of our knowledge and belief. However, we cannot accept liability for the accuracy and entirety of the information provided, except in the case of gross negligence. In particular it should be noted that statements do not have the legal quality of assurances or assured properties. We are grateful for any feedback on the contents.

February 2017

All rights to this publication are reserved by Pilz GmbH & Co. KG. We reserve the right to amend specifications without prior notice. Copies may be made for the user's internal purposes. The names of products, goods and technologies used in this manual are trademarks of the respective companies.

# Contents

<b>1. Useful documentation .....</b>	<b>5</b>
1.1. Documentation from Pilz GmbH & Co. KG .....	5
1.2. Documentation from other sources of information .....	5
<b>2. Hardware configuration.....</b>	<b>6</b>
2.1. Pilz products.....	6
2.2. Hardware configuration .....	6
<b>3. Application task .....</b>	<b>7</b>
3.1. Description .....	7
3.1.1. Light curtain monitoring function .....	7
3.1.2. Feedback loop monitoring function .....	9
3.2. Functional safety .....	10
3.2.1. Safety-related characteristics in accordance with EN ISO 13849-1 .....	10
3.2.2. Safety-related characteristics in accordance with EN 62061 .....	11
3.3. PAS-Project.....	12
3.3.1. IEC 61131 Programming .....	12
3.3.1.1. LD-Editor .....	13
3.3.2. I/O Mapping.....	19
3.3.2.1. I/O Mapping Editor .....	19
3.3.3. Resource Assignment.....	20
3.3.4. Process PAS Project.....	21
3.4. Circuit diagram of the application.....	24
3.4.1. Circuit diagram 1/5.....	24
3.4.2. Circuit diagram 2/5.....	25
3.4.3. Circuit diagram 3/5.....	26
3.4.4. Circuit diagram 4/5.....	27
3.4.5. Circuit diagram 5/5.....	28
<b>4. Table of figures .....</b>	<b>29</b>

## Abbreviations

PAS	<b>P</b> ilz <b>A</b> utomation <b>S</b> uite (software platform)
PSS	Programmable control system (DE: <b>P</b> rogrammierbares <b>S</b> teuerungssystem)
PNOZ	Pilz E-STOP Positive-guided (DE: <b>P</b> ilz <b>N</b> OT-AUS- <b>Z</b> wangsgeführt)
POU	<b>P</b> rogram <b>O</b> rganization <b>U</b> nit
PRG	<b>P</b> rogram
FB	<b>F</b> unction <b>B</b> lock
FUN	<b>F</b> unction
LD	<b>L</b> adder <b>D</b> igram

# 1. Useful documentation

Reading the documentation listed below is necessary for understanding this application note. The availability of the indicated tools and safe handling are also presupposed with the user.

## 1.1. Documentation from Pilz GmbH & Co. KG

No.	Description	Item No.
1	Pilz international homepage, download section	<a href="http://www.pilz.com">www.pilz.com</a>
2	Operating Manual PSSu H PLC1 FS SN SD	21939-EN-xx
3	Operating Manual PSSu E F 4DI	21310-EN-xx-xx
4	Operating Manual PSSu E F DI OZ 2	21328-EN-xx
5	Operating Manual PSSu E F 4DO 0.5	21316-EN-xx
6	Operating Manual PSSu E S 4DI	21 340-EN-xx
7	Operating Manual PSSu E S 4DO 0.5	21 346-EN-xx
8	System Description Programmable safety and control system PSS 4000	1001467-EN-xx
9	Safety Manual Programmable safety and control system PSS 4000	1001468-EN-xx
10	PAS4000 online help	-
11	Operating Manual PSEN op4F/H-s.../1	1001422-EN-xx

## 1.2. Documentation from other sources of information

No.	Description	Item No.
1		
2		
3		
4		

### Note

The present example (PSS 4000 Light Curtain with PSENopt) is also available in the programming languages [Instruction List](#), [Structured Text](#) and [PASmulti](#).

## 2. Hardware configuration

### 2.1. Pilz products

No.	Descriptions	Order number	Version	Number
1	PSSu H PLC1 FS SN SD	312070	002	1
2	PSSu E F 4DI	312200	-	1
3	PSSu E F DI OZ 2	312220	-	1
4	PSSu E F 4DO 0.5	312210	-	2
5	PSSu E S 4DI	312400	-	1
6	PSSu E S 4DO 0.5	312405	-	1
7	PSSu BP 1/8 C	312601	-	6
8	PSEN op4F/H-s-14-090/1	630745	-	2
9	PAS4000	-	1.14.2	1

### 2.2. Hardware configuration

Fig. 1: Hardware configuration

## 3. Application task

### 3.1. Description

The example shows the implementation of a light curtain application with a PSS 4000 PLC. The safe control and evaluation of the signals is taken over by two Pilz Function blocks (FS-FB) from the library.

- ▶ FS\_LightCurtain  
CRC 5963
  
- ▶ FS\_OutputFBL  
CRC B3A7

The workflow is divided into the following two main functions:

- ▶ Light Curtain and
- ▶ Feedback Loop Monitoring

#### 3.1.1. Light curtain monitoring function

The control system monitors the light curtain (B1, B2) via the user program. An instance of the Pilz function block “*FS\_LightCurtain*” is assigned to them. This FS-FB detects whether the assigned light curtain has been operated, as well as detecting incorrect input signals and whether the contact synchronization time has been exceeded, etc.

If the light curtain is interrupted or an error occurs, the enable output “*Enable*” on the FS-FB will immediately be reset.

The enable output “*Enable*” is also reset when the PSS is stopped and when the PSS is switched on. The signal from the enable output “*Enable*” must be evaluated by the user program and trigger an appropriate reaction.

Based on the diagnostic outputs (“*DiagSwitchError*”, “*DiagInputNotValid*”) it can be determined why “*Enable*” was reset.

The outputs “*DiagOperated*”, “*DiagReadyForReset*” and “*DiagReadyForTest*” are used as status messages.

A “*valid bit*” is formed by the system for the respective hardware input to determine whether a process value received from a sensor is valid.

The valid bit is queried in the Function block and indicates whether an error has occurred in the signal transmission between hardware input and processor (such as test clock error, module overheats, etc.).

If the valid bit is FALSE, the process value is invalid and the Pilz function block provides an appropriate diagnostic message. The error signal reset enable.

(For more information, see “*Validity process data*” in *PAS4000 online help*)

The way in which the error is reset will depend on the operating mode set on the FS-FB. In this application example, parameters for FS-FB have been set in such a way that “Reset” (S3) is required in order to reset output parameter “Enable” when:

- the PSS is cold started (PSS switched from off to on),
- warm started (PSS transferring from STOP to RUN) or
- when the light curtain are released.

Although the light curtain and the light curtain function are configured to reset themselves, a PSS cold start or the release of the light curtain may not directly enable a machine to start up without further conditions being met.

#### **Input circuit safety assessment**

- ▶ A short between 24 VDC and an input circuit on the PSSu module will be detected as an error by the AOPD; the AOPD outputs are shut down.
- ▶ A short between the input circuits on the PSSu module will be detected as an error by the AOPD; the AOPD outputs are shut down.
- ▶ If an operator completely (or even maybe partly) is able to access the dangerous area, a risk analysis should clarify whether an additional, separate “*manual reset function*” is required.

#### **Switch-off delay of the light curtain enable signal**

The enable output “Enable” of the Pilz Function block “FS\_LightCurtain” is passed by means of the Function block “TOF” switch-off-delayed to the Pilz function block “FS\_OutputFBL”.

This allows the controlled switch-off of the motor before the safe isolation of the motor contactor is activated. The shutdown delay must be considered at the risk assessment.



### 3.1.2. Feedback loop monitoring function

The control system monitors the feedback circuits (NC contacts) of the motor contactors KM1 and KM2 via the user program.

An instance of the Pilz function block “*FS\_OutputFBL*” is assigned to them.

The FS\_FB drives the contactors as well as monitoring the feedback loop.

A 1-signal at input parameter “*Input*” of the FS-FB sets the outputs that drive the contactors, “*Output1*” and “*Output2*”, to “1”; a 0-signal sets it to “0”.

If an error occurs, the outputs “*Output1*” and “*Output2*” that drive the contactors on FS-FB will immediately be reset. Both outputs are also reset when the PSS is stopped and when the PSS is switched on.

Based on the diagnostic outputs (“*DiagFeedbackLoopError*”, “*DiagFeedbackLoopNotValid*”) it can be determined why the outputs were reset.

A “*valid bit*” is formed by the system for the respective hardware input to determine whether a process value received from a sensor is valid.

The valid bit is queried in the Function block and indicates whether an error has occurred in the signal transmission between hardware input and processor (such as test clock error, module overheats, etc.).

If the valid bit is FALSE, the process value is invalid and the Pilz function block provides an appropriate diagnostic message. The error signal reset enable.

(For more information, see “*Validity process data*” in PAS4000 online help)

If an error occurs, a new activity has to take place at the input “*Input*” of the FS-FB once the error has been rectified, so that the outputs “*Output1*” and “*Output2*” will be set again.

#### Feedback loop monitoring safety assessment

- ▶ A short between 24 VDC and a safety output or a feedback loop input will be detected as an error by the programmable safety system. The load can be switched off via the second shutdown route.
- ▶ The feedback loop contacts must be installed in a single mounting area (control cabinet).
- ▶ To achieve a higher level of safety, 2 actuators must be used.

### 3.2. Functional safety

#### 3.2.1. Safety-related characteristics in accordance with EN ISO 13849-1

No.	Safety function	PL	Safety-related parts of the control system
1	Machine shut down when the safety light curtain is interrupted	PL e	Sensor (PSEN op4F-s.../1 A1, A2) Input (PSSu E F 4DI) Logic (PSSu H PLC1 FS SN) Output (PSSu E F DI OZ 2) Actuator (contactors KM1, KM2)

#### Prerequisites

No.	Description	Identification	
1	Common cause failure (CCF)	Requirements are considered to be met (must be tested on implementation)	
2	Mission time	20 years	
3	Operating interval (electromechanical components)	Sensor	two operations per hour
		Actuator	two operations per hour
4	Characteristic data of contactors KM1/KM2	B10d	1,300,000

Please note the further requirements of EN ISO 13849-1, e.g. requirements for avoiding systematic faults.

### 3.2.2. Safety-related characteristics in accordance with EN 62061

No.	Safety-related control function (SRCF)	Safety Integrity Level	Subsystems
1	Machine shut down when the safety light curtain is interrupted	SIL 3	Sensor (PSEN op4F-s.../1 A1, A2) Input (PSSu E F 4DI) Logic (PSSu H PLC1 FS SN) Output (PSSu E F DI OZ 2) Actuator (contactors KM1, KM2)

#### Prerequisites

No.	Description	Identification
1	Common cause failure (CCF)	$\beta = 2\%$ (must be tested on implementation)
2	Proof test interval	20 years
3	Operating interval (electromechanical components)	Sensor two operations per hour
		Actuator two operations per hour
4	Characteristic data of contactors KM1/KM2	B10d 1,300,000
		Dangerous failure rate 75%

Please note the further requirements of EN 62061, e.g. requirements for systematic safety integrity.

### 3.3. PAS-Project

To operate a plant with one or more programmable control systems PSS 4000, a project must be created in PAS4000.

A project consists of the hardware configuration and the user program.

#### 3.3.1. IEC 61131 Programming

When programming in accordance with IEC 61131, the user program is structured by three types of program organisation units (POUs):

- ▶ Programs (PRG)
- ▶ Function blocks (FB)
- ▶ Functions (FUN)

The program forms the higher structural level. Functions and function blocks may be called up within a program.

Function blocks and functions undertake specific individual tasks within the program.

Each POU consists of a declaration part and an instruction part.

The variables and type declarations are made in the declaration part, which is shown in text format, irrespective of the programming language.

The instruction part contains the instructions. The instructions can be formulated in one of the IEC 61131 programming languages.

*(For more information about programming with PAS4000, look at PAS4000 online help)*

### 3.3.1.1. LD-Editor

The program for the cyclic process is created in a POU of the type “*Program*”.

The Pilz function blocks for light curtain and feedback loop were added from the library. (Right-click in the declaration part of the POU “*Add library element*”).

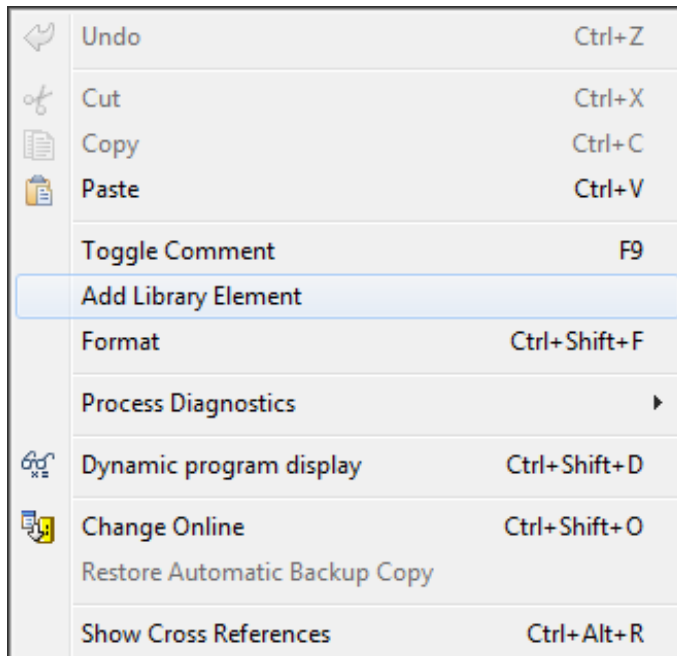


Fig. 2: Add library element

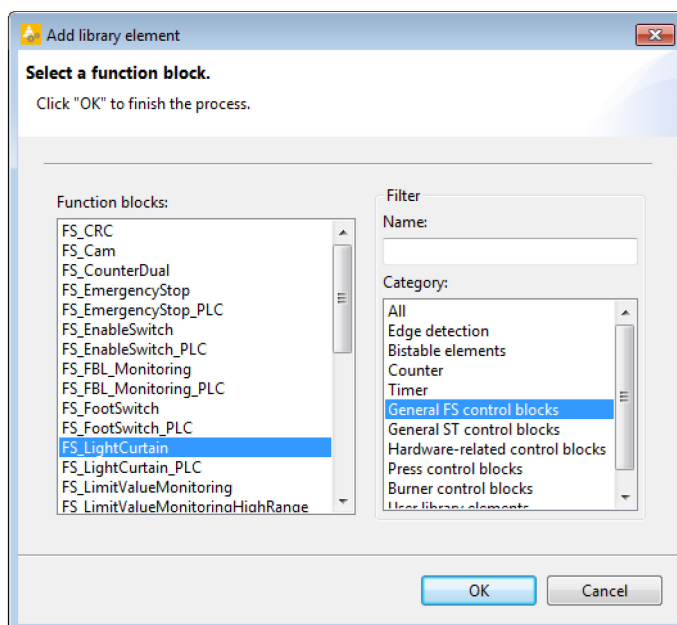


Fig. 3: Selection library element

Use the Palette to add functions to the ladder network by clicking once on the item in the Palette and then once in the ladder network.

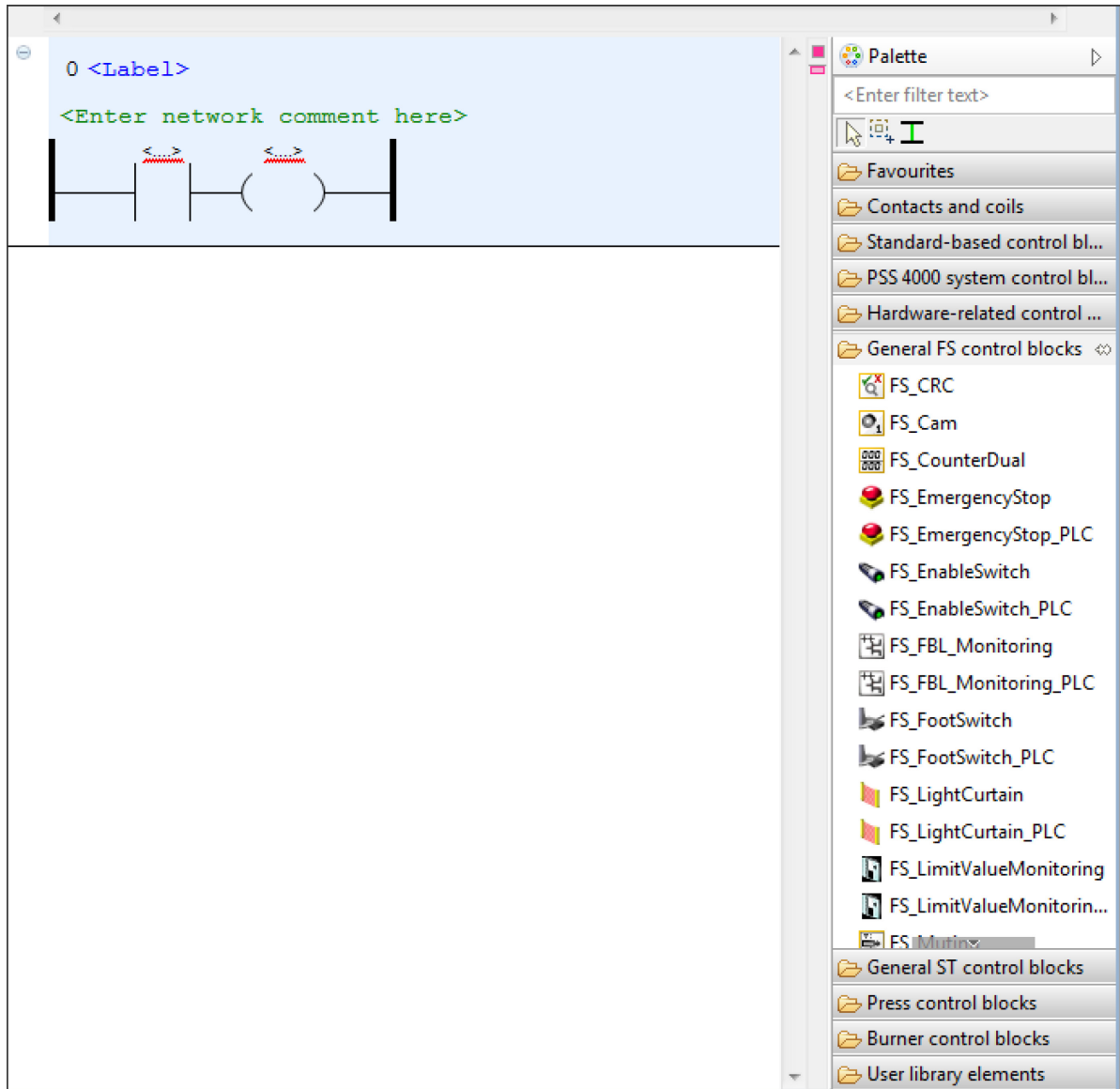


Fig. 4: Edit LD network

Add additional networks by right clicking under the existing network and select "Add network".

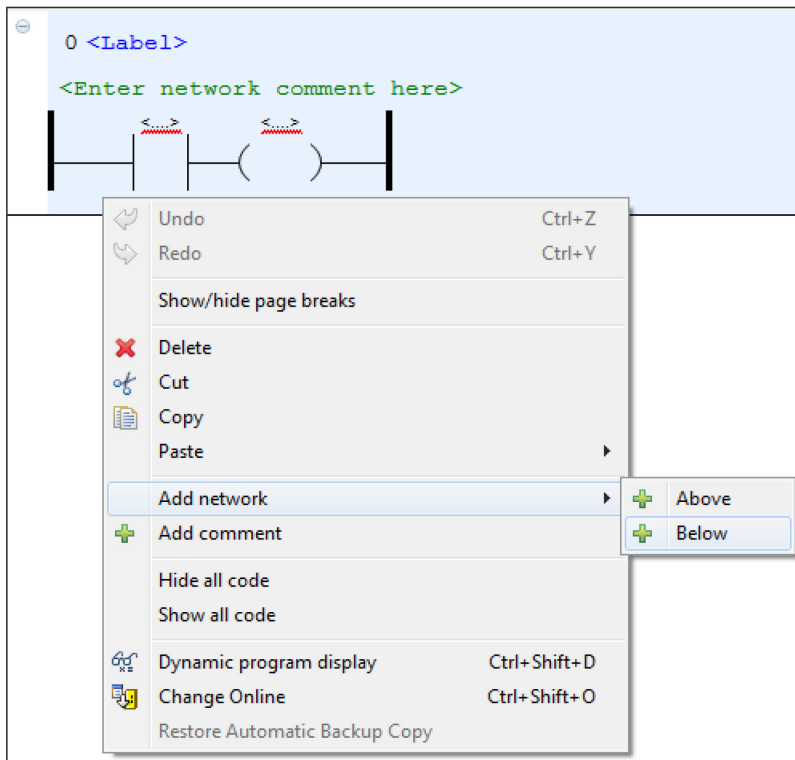


Fig. 5: Add additional ladder network

Invert/Negate existing contacts.coils in the network by right clicking on the contact and selecting "Invert negation".

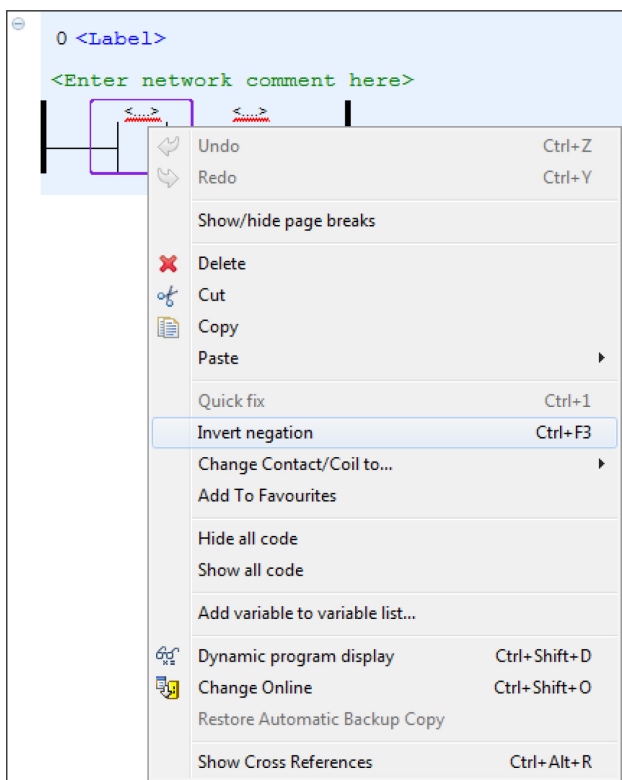


Fig. 6: Invert negation

**Failsafe program****Declaration part**

```

PROGRAM Main_FS
VAR
    // Light Curtain 1
    MyLightCurtain1                : FS_LightCurtain;
    MyLCurt1_Enable                 AT %Q* : SAFEBOOL;
    MyLCurt1_DiagOperated           AT %Q* : SAFEBOOL;
    MyLCurt1_DiagReadyForReset     AT %Q* : SAFEBOOL;
    MyLCurt1_DiagReadyForTest     AT %Q* : SAFEBOOL;
    MyLCurt1_DiagSwitchError       AT %Q* : SAFEBOOL;
    MyLCurt1_DiagInputNotValid    AT %Q* : SAFEBOOL;

    // Switch-off delay
    MyTOF                           : TOF;
    MyLCurt1_Enable_Delayed         : SAFEBOOL;
    ET_Time                          : TIME;

    // FBL
    MyFeedBackLoop1                : FS_OutputFBL;
    MyFBL1_DiagFblError             AT %Q* : SAFEBOOL;
    MyFBL1_DiagFblNotValid         AT %Q* : SAFEBOOL;
END_VAR

VAR CONSTANT
    // Declaration SwitchType 3 (NCNC)
    MyLCURT1_DOUBLE_CH              : USINT := USINT#3;
END_VAR

```



Instruction part

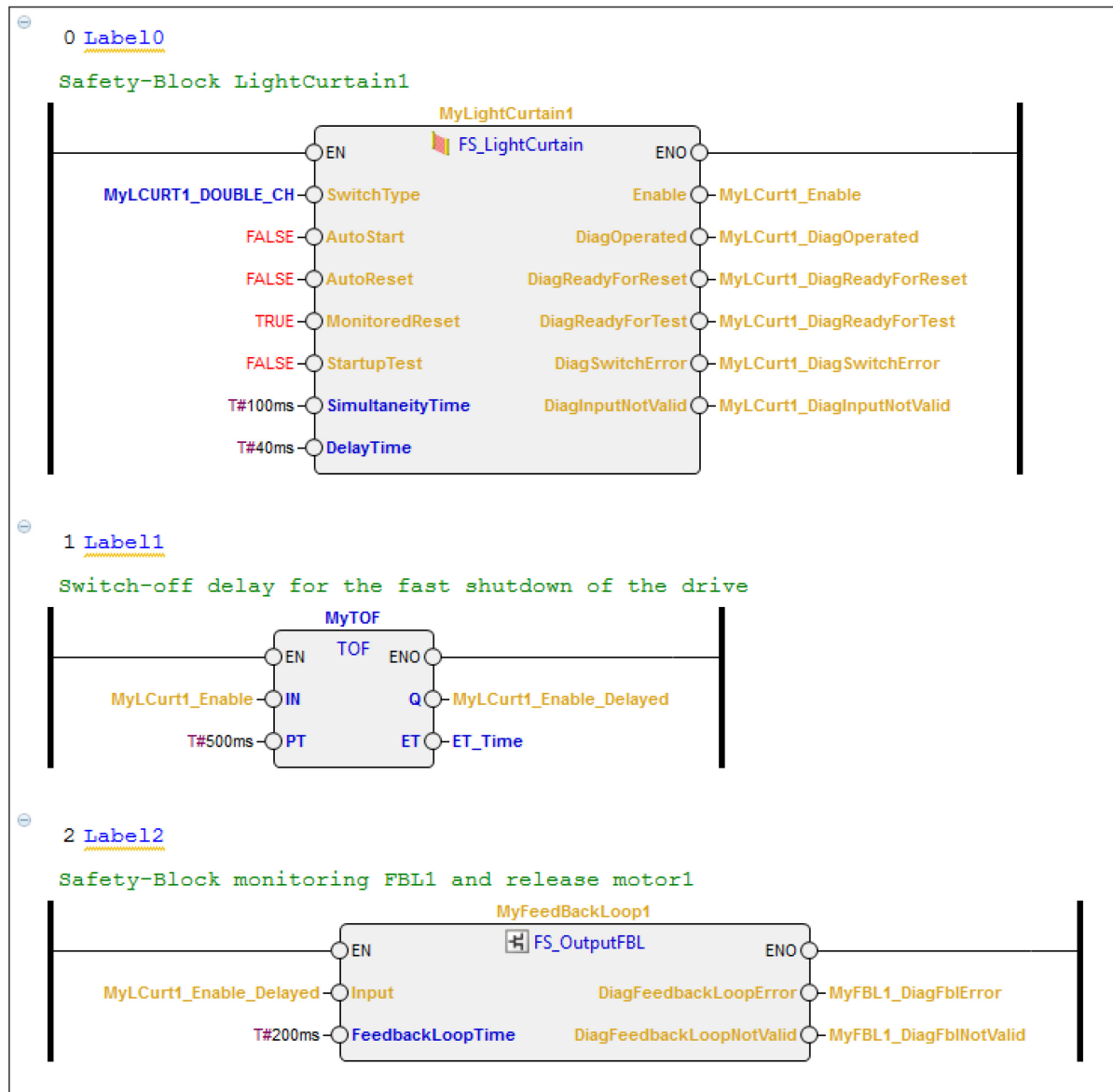


Fig. 7: Instruction part

### Standard program

The signals from the start- and stop switch are imported from a standard module PSSu E S 4DI (1A4). These signals belong to the motor control and thus to the standard control functions of the machine.

The program code for the evaluation and processing of these signals is not processed within the FS resource (safety-related part) of the control, but in a ST resource in a separate task as independent application (additional POU of type program).

```

PROGRAM Main_ST
VAR
  MyRisingEdge1                : R_TRIG;
  Start                        AT %I* : BOOL;
  Stop                         AT %I* : BOOL;
  Motor1_On                   AT %Q* : BOOL;
  Other_Stop_Conditions       AT %I* : BOOL;
  FF_Motor1On                 : RS;
  Q_Edge1                     : BOOL;
  Reset_Motor                 : BOOL;
END_VAR

```

### Instruction part

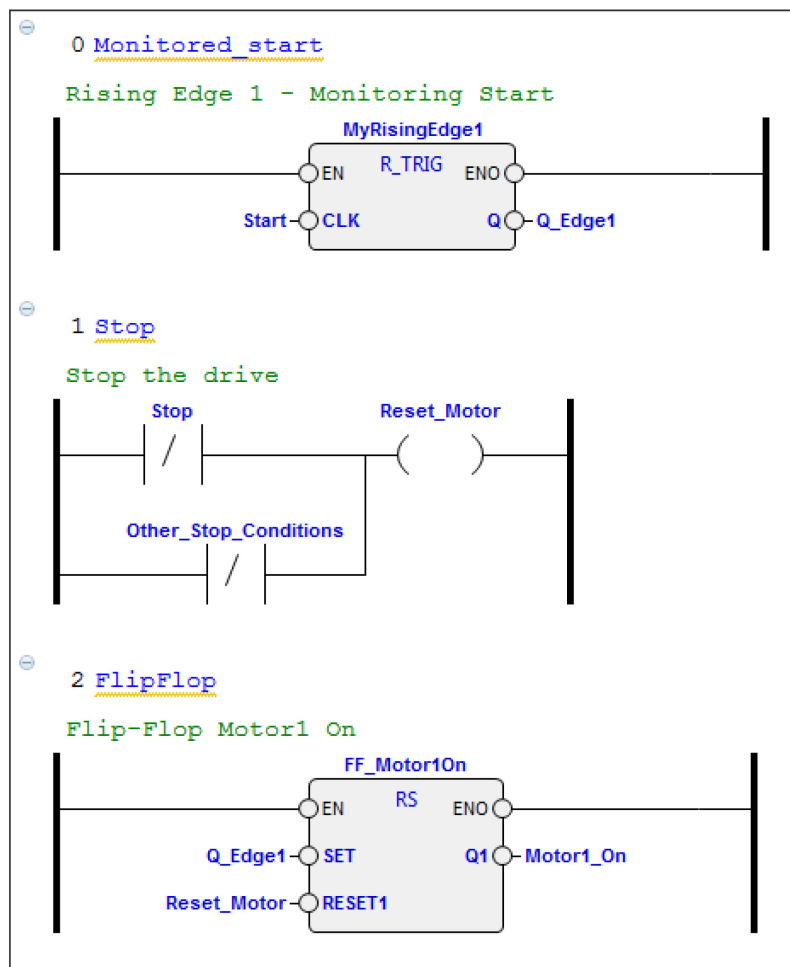


Fig. 8: Instruction part

### 3.3.2. I/O Mapping

In PAS4000, variables can be created and the user program can be programmed without the need of the mapping to the hardware being present at the beginning of the project.

After identification of the used I/O from the variable declaration, the required hardware can be determined.

The I/O mapping editor forms the connecting between the user program and the hardware and coordinates the available I/O and existing PI-variables.

#### 3.3.2.1. I/O Mapping Editor

The PI variables declared in the user-program can be assigned in the I/O mapping editor to the hardware configuration.

The screenshot displays the I/O Mapping Editor interface for an LD-program. It is divided into two main panes: 'PI variables' on the left and 'I/O data module bus' on the right.

**PI variables pane:** Shows a project tree for 'AN\_1004295\_EN\_LD'. Under 'IEC 61131 programming' > 'Main\_FS', the following variables are listed:

- MyLightCurtain1.Reset : **BOOL** (red)
- MyFeedBackLoop1.FeedbackLoop : **SAFEBOOL** (green)
- MyLightCurtain1.InputNC1 : **SAFEBOOL** (green)
- MyLightCurtain1.InputNC2 : **SAFEBOOL** (green)
- MyFBL1\_DiagFblError : **SAFEBOOL** (green)
- MyFBL1\_DiagFblNotValid : **SAFEBOOL** (green)
- MyFeedBackLoop1.Output1 : **SAFEBOOL** (green)
- MyFeedBackLoop1.Output2 : **SAFEBOOL** (green)
- MyLCurt1\_DiagInputNotValid : **SAFEBOOL** (green)
- MyLCurt1\_DiagOperated : **SAFEBOOL** (green)
- MyLCurt1\_DiagReadyForReset : **SAFEBOOL** (green)
- MyLCurt1\_DiagReadyForTest : **SAFEBOOL** (green)
- MyLCurt1\_DiagSwitchError : **SAFEBOOL** (green)
- MyLCurt1\_Enable : **SAFEBOOL** (green)

Under 'Main\_ST', the following variables are listed:

- Other\_Stop\_Conditions : **BOOL** (red)
- Start : **BOOL** (red)
- Stop : **BOOL** (red)
- Motor1\_On : **BOOL** (red)

**I/O data module bus pane:** Shows the hardware configuration for 'AN\_1004295\_EN\_LD' > 'APPLICATION\_PLC' > 'Module bus PSSu'. It lists several modules and their I/O points:

- 0 : PSSu E F 4DI
  - I0(11) : **FS\_I\_DI** (green)
  - I1(21) : **FS\_I\_DI** (green)
  - I2(14) : **FS\_I\_DI** (green)
  - I3(24) : **FS\_I\_DI** (green)
- 1 : PSSu E F DI OZ 2
  - Q0(11,21) : **FS\_O\_DO** (red)
  - I0(14) : **FS\_I\_DI** (green)
  - I1 : **FS\_I\_DI** (green)
- 2 : PSSu E F 4DO 0.5
  - Q0(11) : **FS\_O\_DO** (red)
  - Q1(21) : **FS\_O\_DO** (red)
  - Q2(14) : **FS\_O\_DO** (red)
  - Q3(24) : **FS\_O\_DO** (red)
- 3 : PSSu E F 4DO 0.5
  - Q0(11) : **FS\_O\_DO** (red)
  - Q1(21) : **FS\_O\_DO** (red)
  - Q2(14) : **FS\_O\_DO** (red)
  - Q3(24) : **FS\_O\_DO** (red)
- 4 : PSSu E S 4DI
  - I0(11) : **ST\_I\_DI** (green)
  - I1(21) : **ST\_I\_DI** (green)
  - I2(14) : **ST\_I\_DI** (green)
  - I3(24) : **ST\_I\_DI** (green)
- 5 : PSSu E S 4DO 0.5
  - Q0(11) : **ST\_O\_DO** (red)
  - Q1(21) : **ST\_O\_DO** (red)
  - Q2(14) : **ST\_O\_DO** (red)
  - Q3(24) : **ST\_O\_DO** (red)

Fig. 9: Mapping Editor – LD-program

### 3.3.3. Resource Assignment

The resource assignment defines which section of the user program is to be executed on which resource and in which task. The tasks determine the run program, i.e. the priority with which they are executed.

You can assign the programs automatically to task by drag and drop, then modify the task settings, or create the tasks first and then assign the programs separately.

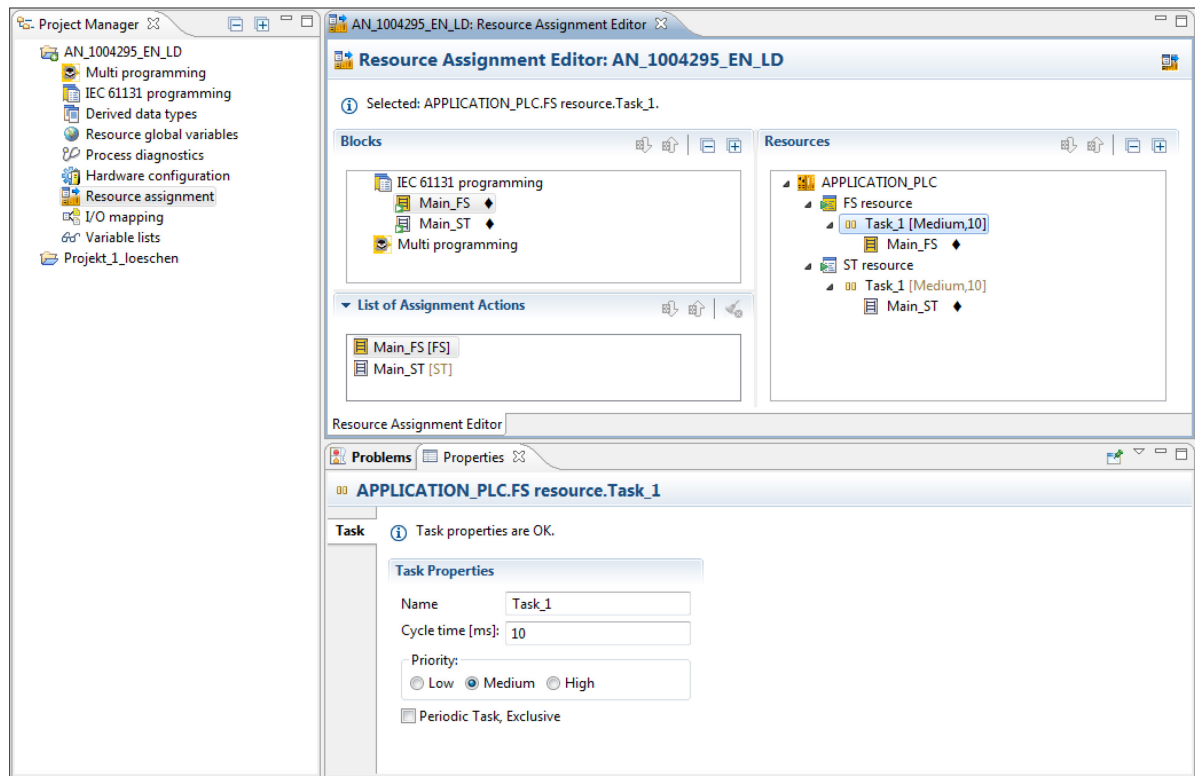


Fig. 10: Resource Assignment

### 3.3.4. Process PAS Project

- ▶ Step 1: In the LD editor, PI variables and the logical sequence will be generated as a program.

```

Main_FS
PROGRAM Main_FS
VAR
  MyLCurt1_DiagInputNotValid AT %Q* : SAFEBOOL;
  MyFBL1_DiagFblError       AT %Q* : SAFEBOOL;
    
```

Fig. 11: PI variables

- ▶ Step 2: The design of the hardware (control, I/O, sensor, actor) will created as a circuit diagram. (parallel possible to Step 1)

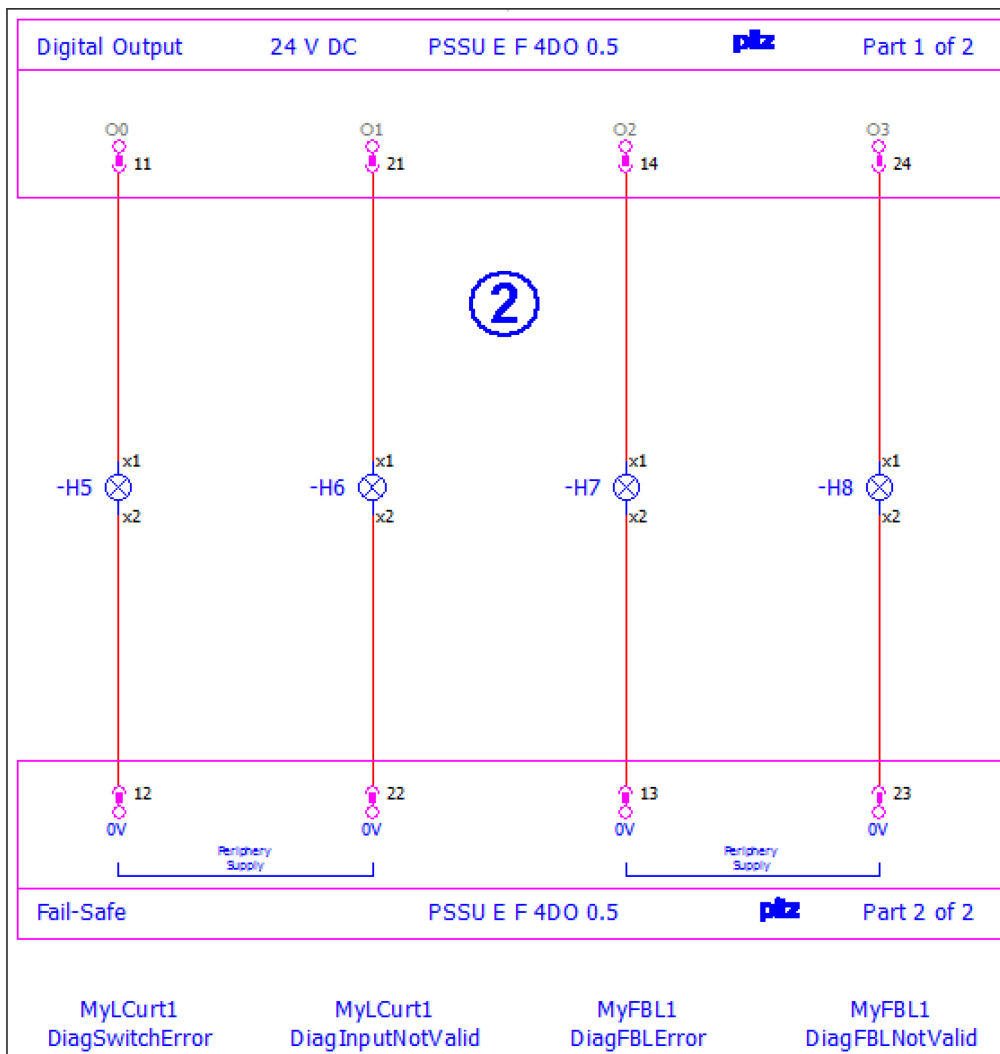


Fig. 12: Circuit diagram (extract)

- ▶ Step 3: Based on the PI variables (I/O), the required power of control (PLC, Multi) is selected. The implementation of the I/O modules in the PAS system occurs in the PSSu module editor.

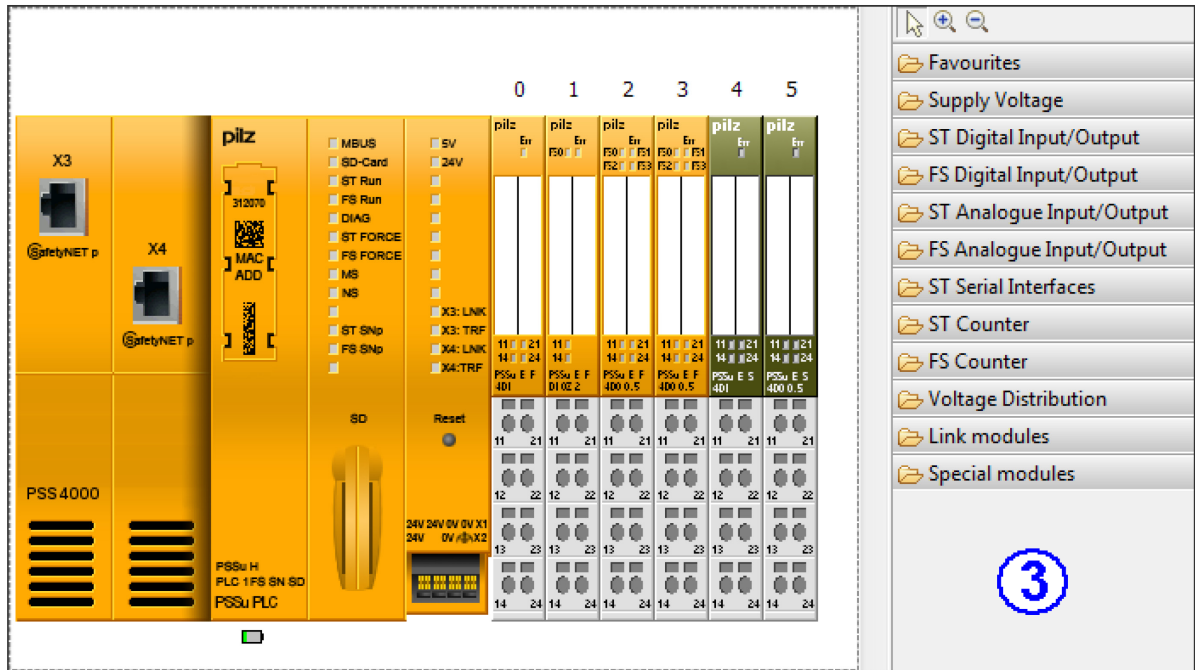


Fig. 13: PSSu Module Editor

- ▶ Step 4: Assignment of the PI variables in the I/O Mapping Editor.

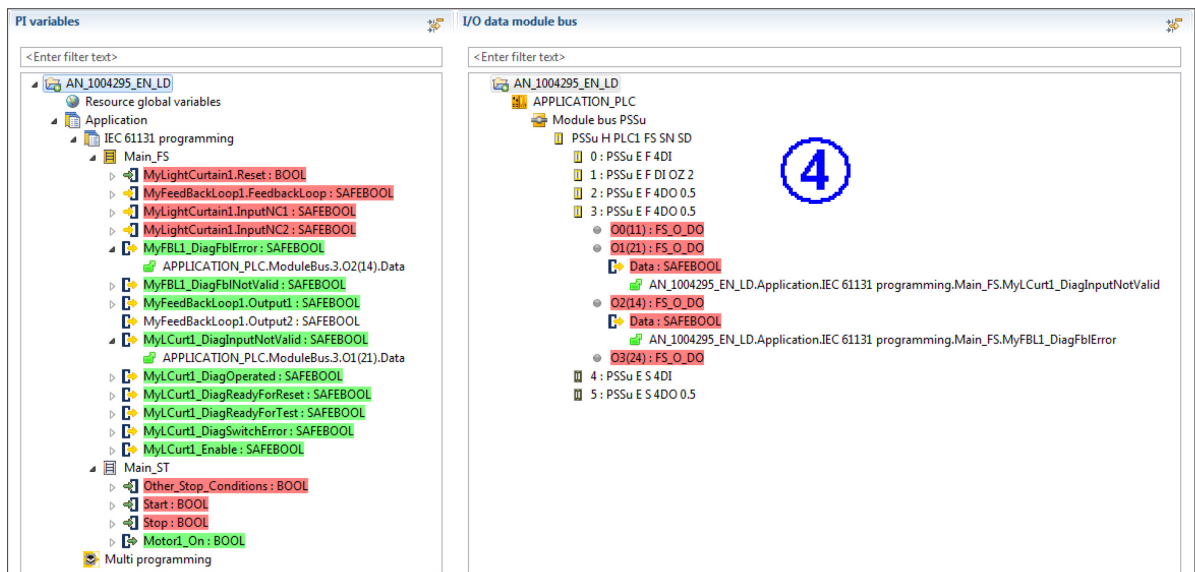


Fig. 14: I/O Mapping Editor

► Overview process PAS Project (Steps 1-4)

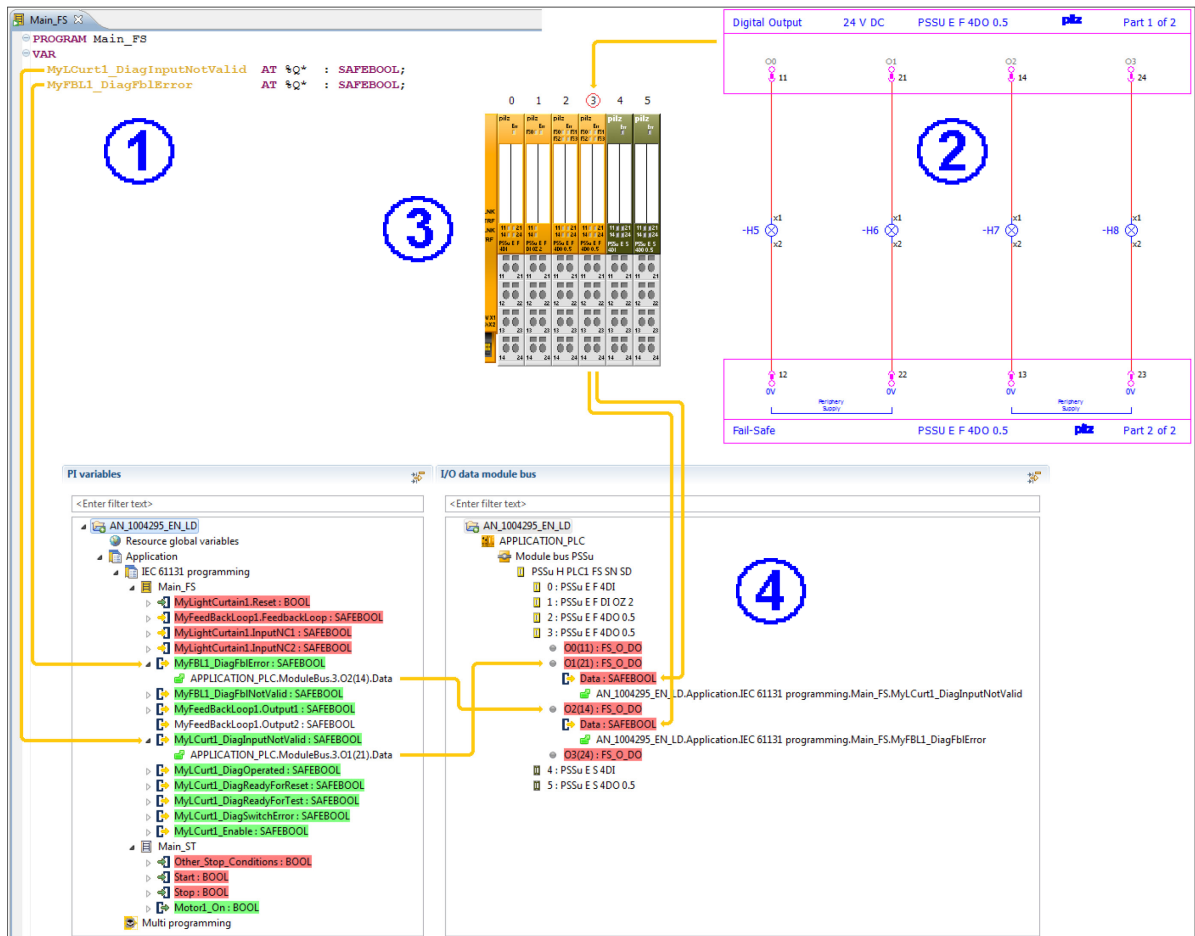
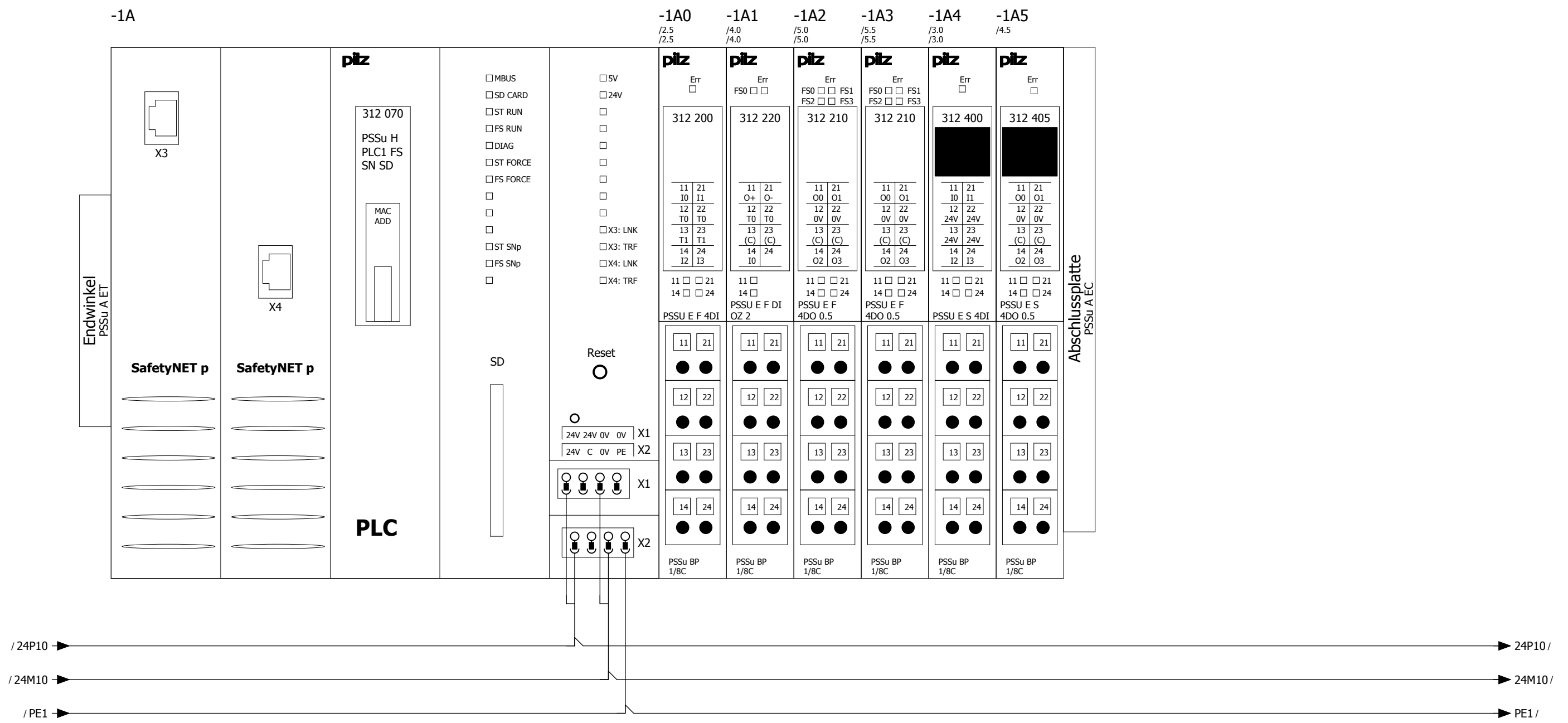
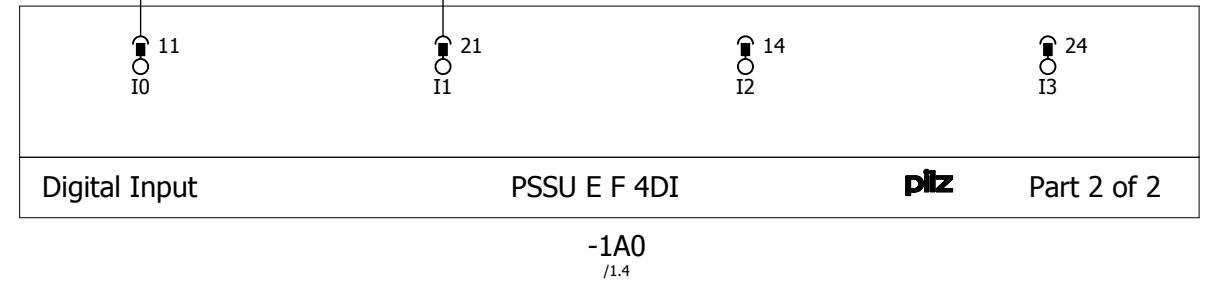
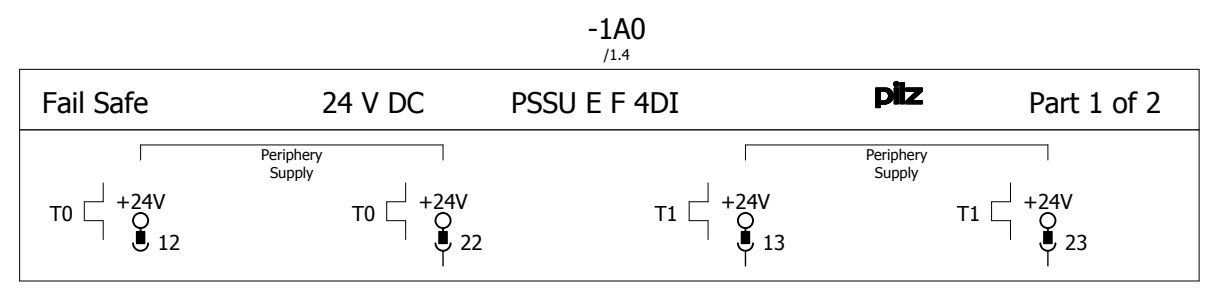
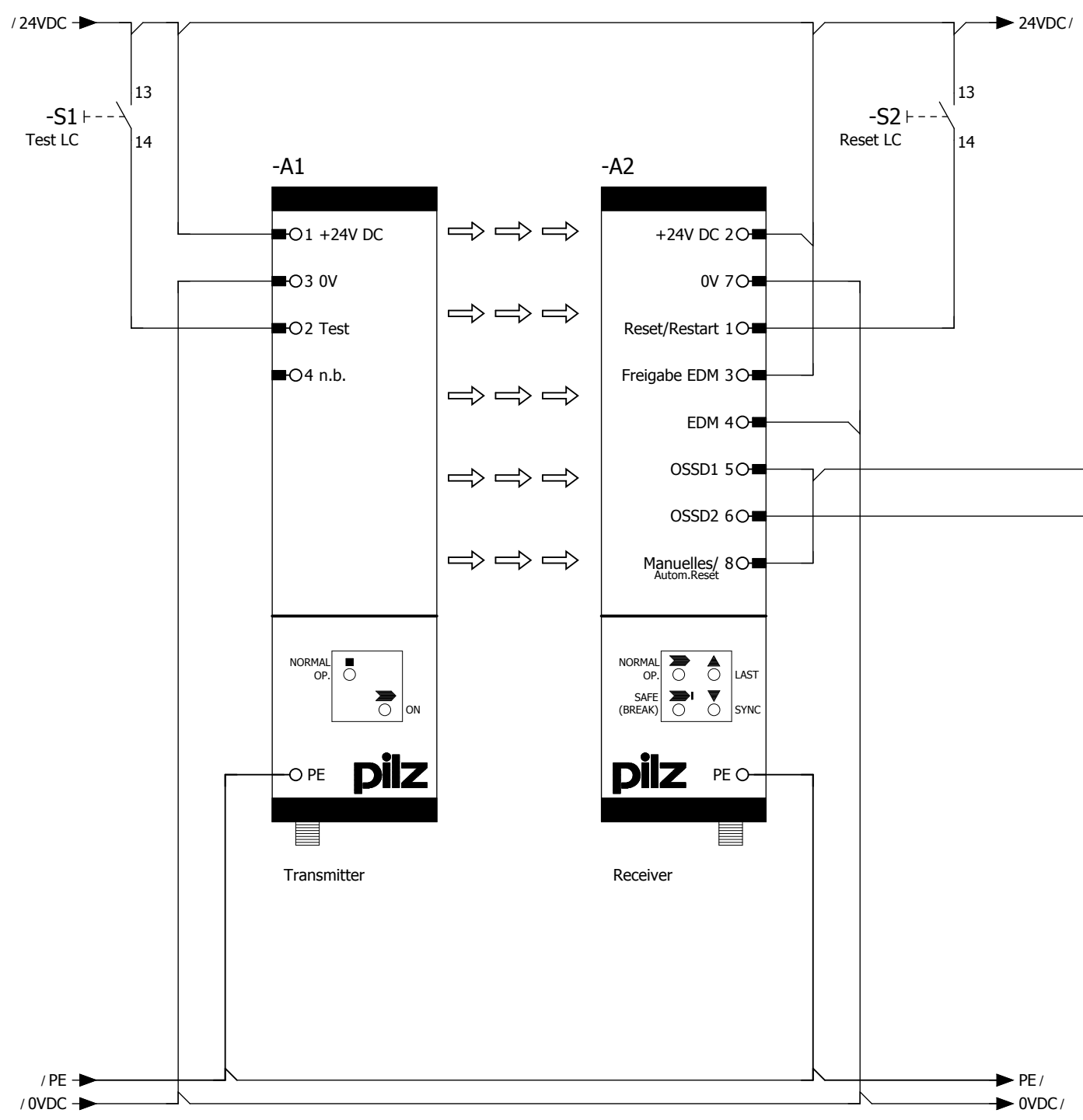


Fig. 15: Process PAS Project



- PSSu E F 4DI 312 200
- PSSu E S 4DI 0.5 312 400
- PSSu E S 4DI 0.5 312 400
- PSSu E F DI OZ 2 312 220
- PSSu E S 4DO 0.5 312 405
- PSSu E S 4DO 0.5 312 405
- PSSu BP 1/8C 312 601
- PSSu BP 1/8C 312 601
- PSSu BP 1/8C 312 601
- PSSu BP 1/8C 312 601
- PSSu BP 1/8C 312 601
- PSSu BP 1/8C 312 601





MyLightCurtain1 InputNC1      MyLightCurtain1 InputNC2      Spare      Spare

Revision	30.09.2016	Date	02.09.2016
Name	Pilz	Name	Pilz
		Dep.	CS

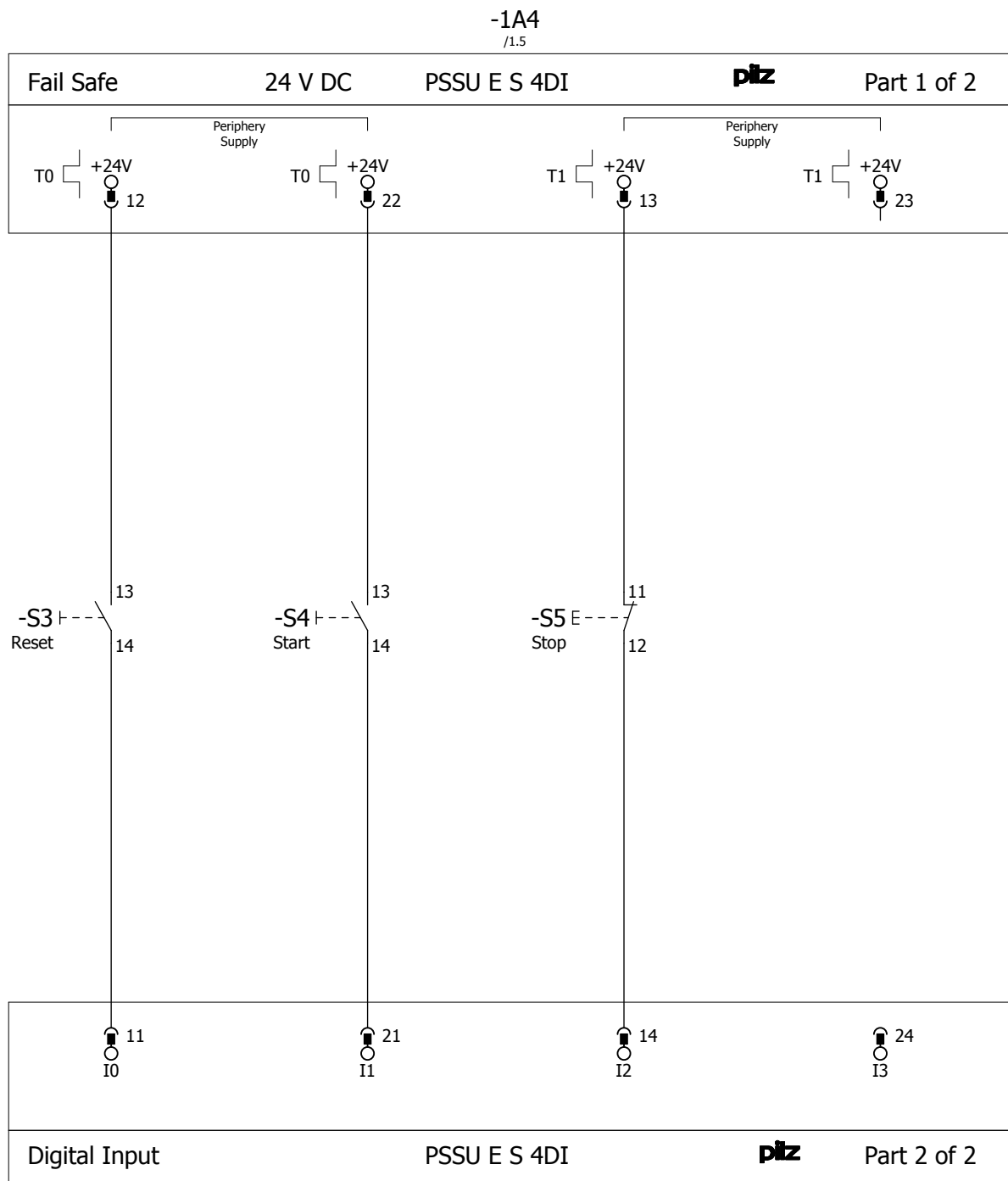
EN ISO 13849-1	PL e
EN 62061	SIL 3



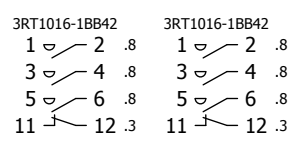
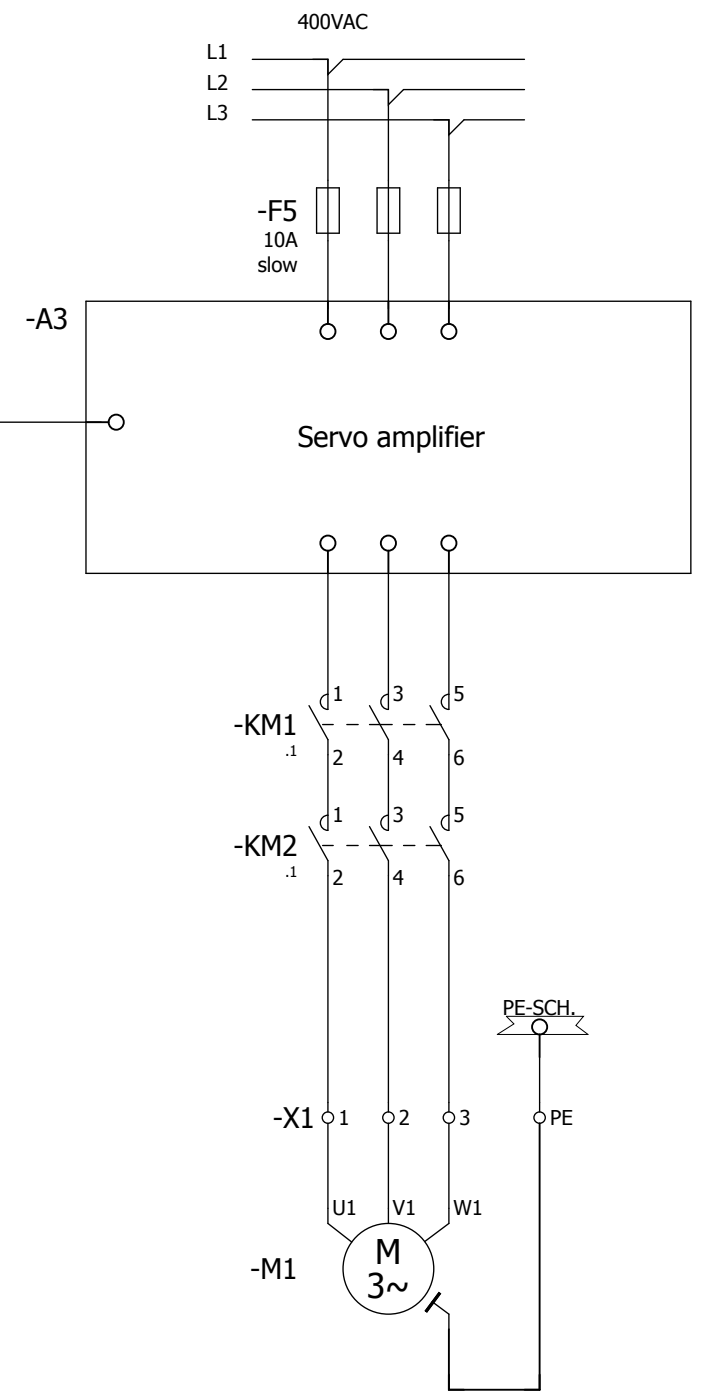
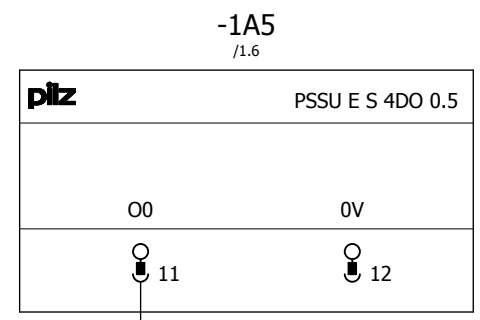
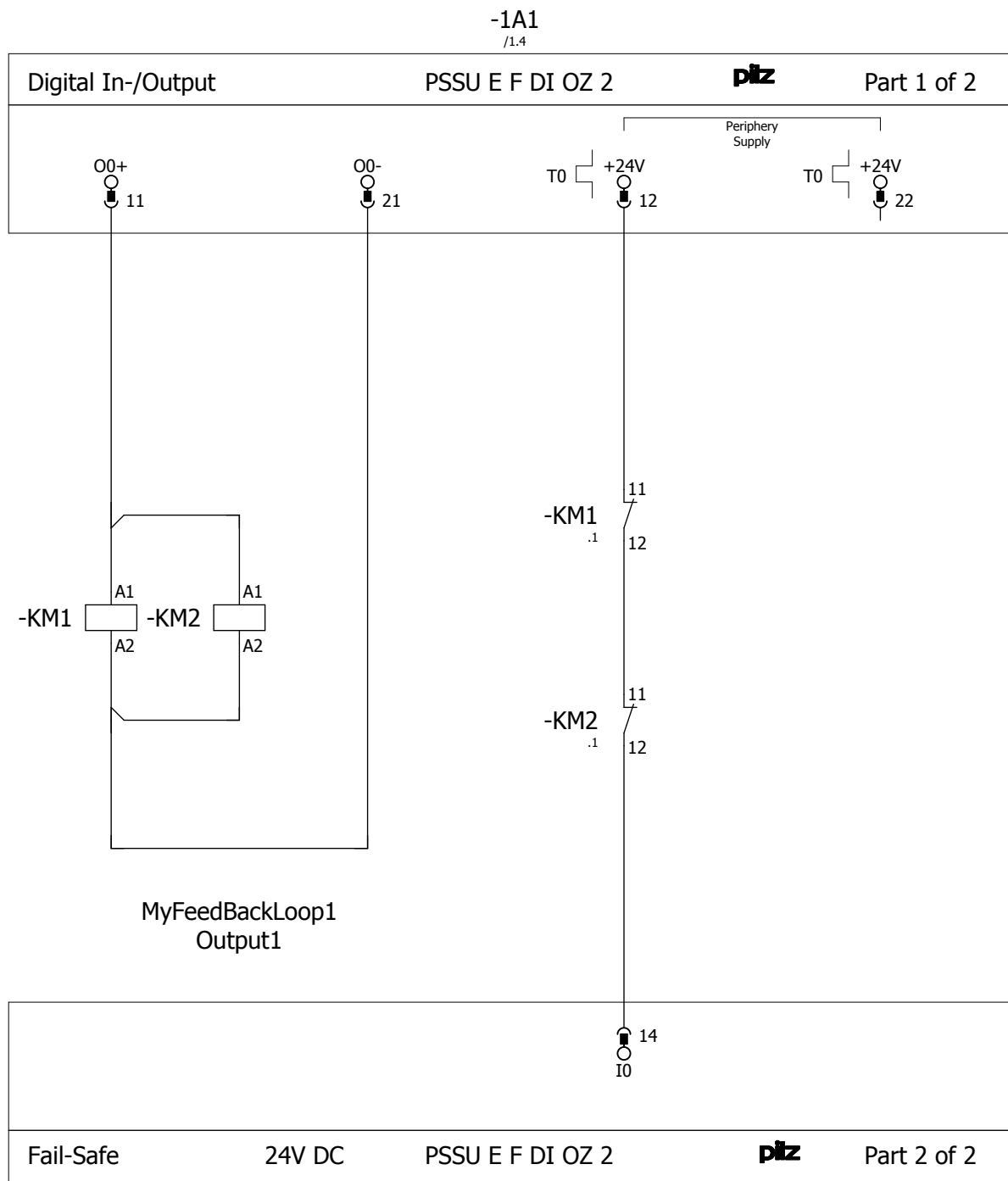
Pilz GmbH & Co.KG  
Felix-Wankel-Str.2  
73760 Ostfildern

PSS 4000 - Light Curtain with PSENopt  
Inputs

Mounting place	+ AN_1004295_01
Page:	2 / 5

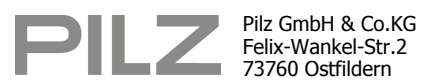


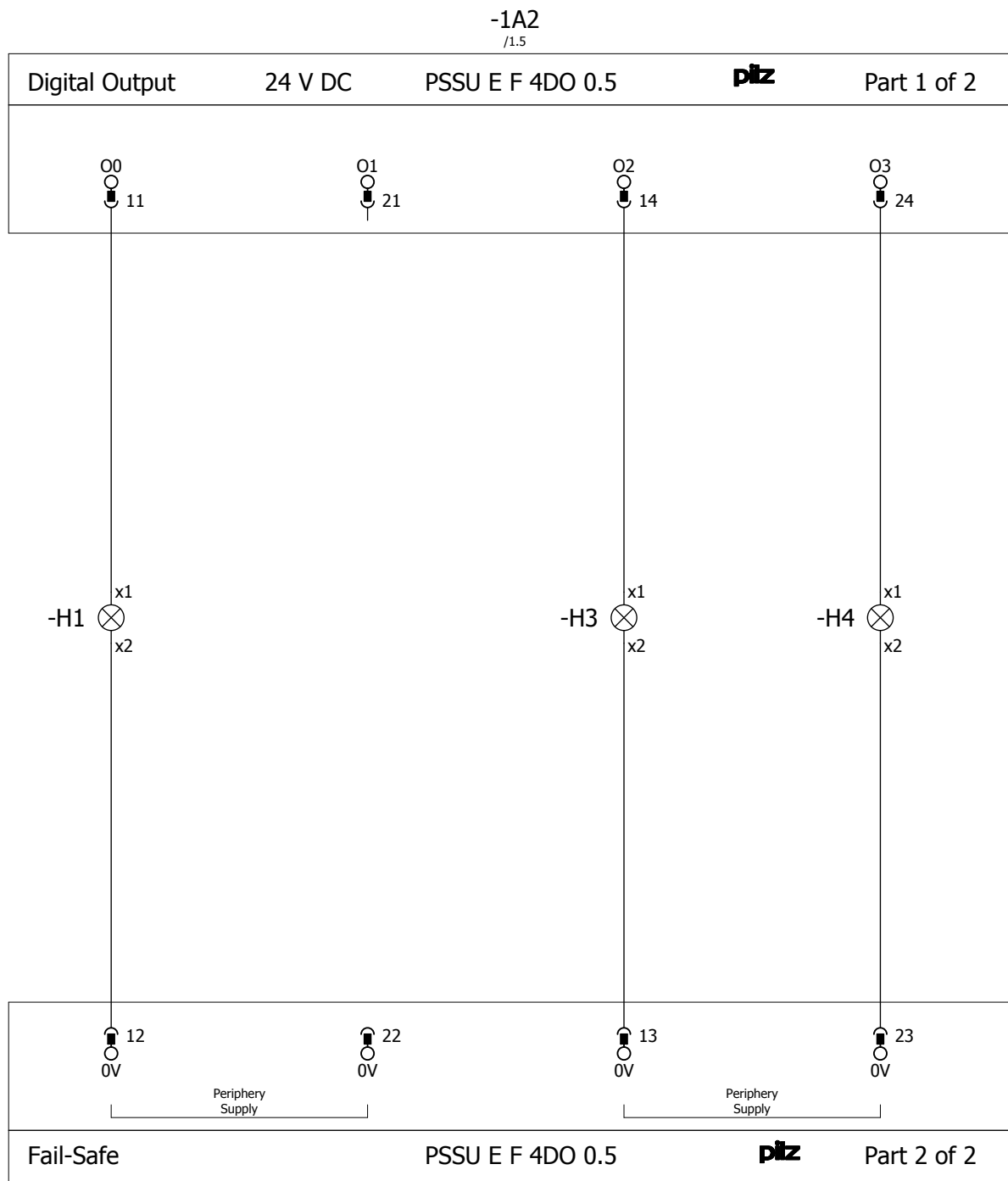
MyLightCurtain1      Start      Stop      Spare  
Reset



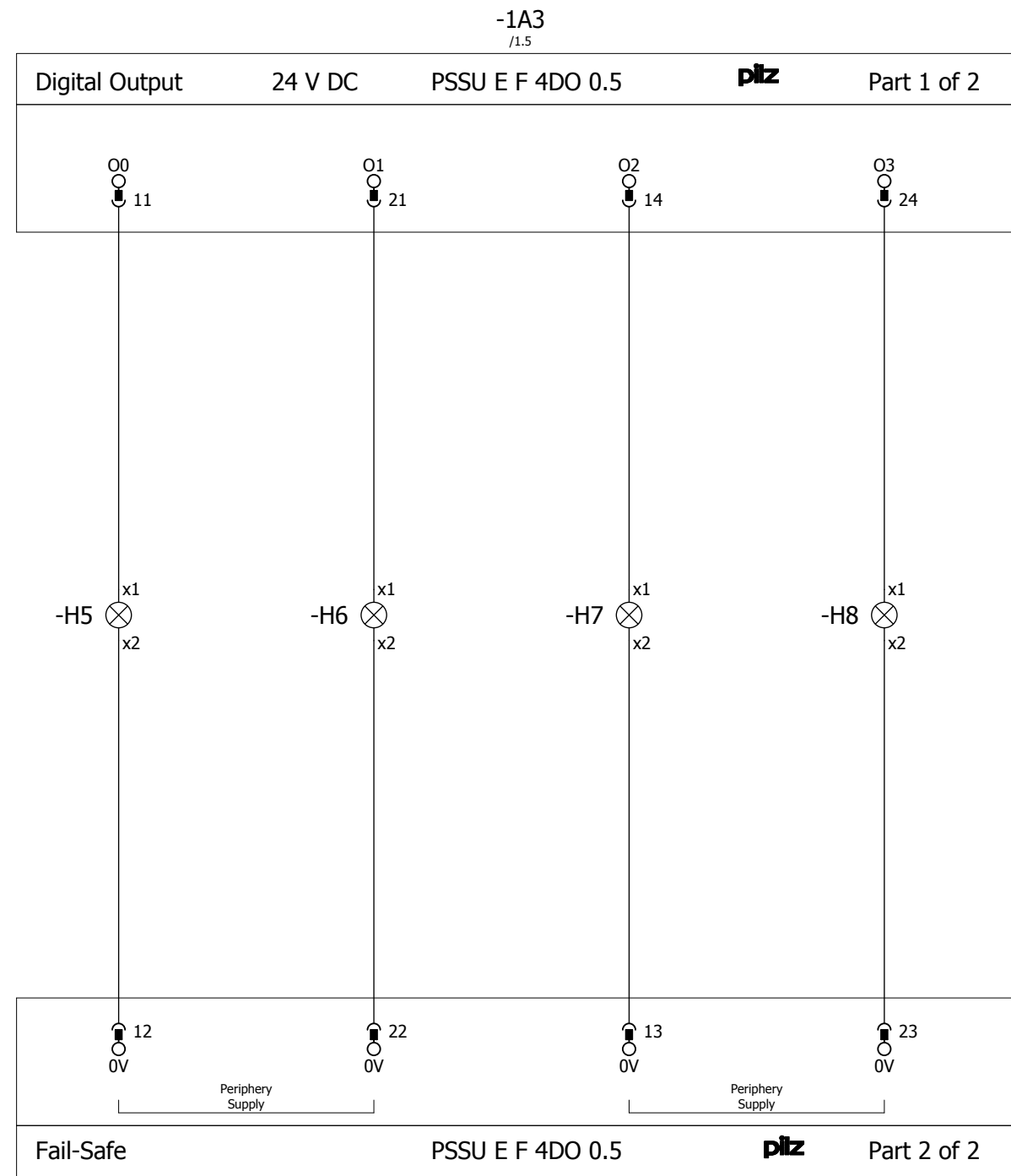
MyFeedBackLoop1  
FeedbackLoop

Motor1\_On





-1A2  
/1.5



-1A3  
/1.5

MyLCurt1  
DiagOperated

Spare

MyLCurt1  
DiagReadyForReset

MyLCurt1  
DiagReadyForTest

MyLCurt1  
DiagSwitchError

MyLCurt1  
DiagInputNotValid

MyFBL1  
DiagFBLError

MyFBL1  
DiagFBLNotValid

Revision	30.09.2016	Date	02.09.2016
Name	Pilz	Name	Pilz
Dep.		CS	

EN ISO 13849-1	PL e
EN 62061	SIL 3



Pilz GmbH & Co.KG  
Felix-Wankel-Str.2  
73760 Ostfildern

PSS 4000 - Light Curtain with PSENopt	Mounting place + AN_1004295_01
Status/Error message	Page: 5 / 5

## 4. Table of figures

**Es konnten keine Einträge für ein Abbildungsverzeichnis gefunden werden.**

Fig. 1: Hardware configuration .....	6
Fig. 2: Add library element .....	13
Fig. 3: Selection library element.....	13
Fig. 4: Edit LD network .....	14
Fig. 5: Add additional ladder network .....	15
Fig. 6: Invert negation.....	15
Fig. 7: Instruction part.....	17
Fig. 8: Instruction part.....	18
Fig. 9: Mapping Editor – LD-program .....	19
Fig. 10: Resource Assignment .....	20
Fig. 11: PI variables.....	21
Fig. 12: Circuit diagram (extract).....	21
Fig. 13: PSSu Module Editor .....	22
Fig. 14: I/O Mapping Editor .....	22
Fig. 15: Process PAS Project.....	23

# ► Support

Technical support is available from Pilz round the clock.

## Americas

Brazil

+55 11 97569-2804

Canada

+1 888-315-PILZ (315-7459)

Mexico

+52 55 5572 1300

USA (toll-free)

+1 877-PILZUSA (745-9872)

## Asia

China

+86 21 60880878-216

Japan

+81 45 471-2281

South Korea

+82 31 450 0680

## Australia

+61 3 95446300

## Europe

Austria

+43 1 7986263-0

Belgium, Luxembourg

+32 9 3217575

France

+33 3 88104000

Germany

+49 711 3409-444

Ireland

+353 21 4804983

Italy

+39 0362 1826711

## Scandinavia

+45 74436332

Spain

+34 938497433

Switzerland

+41 62 88979-30

The Netherlands

+31 347 320477

Turkey

+90 216 5775552

United Kingdom

+44 1536 462203

## You can reach our international hotline on:

+49 711 3409-444

support@pilz.com

Pilz develops environmentally-friendly products using ecological materials and energy-saving technologies. Offices and production facilities are ecologically designed, environmentally-aware and energy-saving. So Pilz offers sustainability, plus the security of using energy-efficient products and environmentally-friendly solutions.



## The 4-fold safety of automation



Pilz GmbH & Co. KG  
 Felix-Wankel-Straße 2  
 73760 Ostfildern, Germany  
 Tel.: +49 711 3409-0  
 Fax: +49 711 3409-133  
 info@pilz.com  
 www.pilz.com



100XXXX-DE-0X  
 0-0-1-3-000, 2015-00 Printed in Germany  
 © Pilz GmbH & Co. KG, 2015

CMSE®, InduratNET p®, PAS4000®, PASscal®, PASconfig®, Pilz®, PIT®, PLID®, PMCPprime®, PMCPprotego®, PMCIendo®, PMD®, PMH®, PNOZ®, Primo®, PSEN®, PSS®, PVIS®, SafetyBUS p®, SafetyEYE®, SafetyNET p®, THE SPIRIT OF SAFETY® are registered and protected trademarks of Pilz GmbH & Co. KG in some countries. We would point out that product features may vary from the details stated in this document, depending on the status at the time of publication and the scope of the equipment. We accept no responsibility for the validity, accuracy and entirety of the text and graphics presented in this information. Please contact our Technical Support if you have any questions.